

**IN THE SPECIFICATION**

Please replace page 2, lines 1-10 with the following replacement paragraph:

An apparatus includes a reception circuit with a frequency synthesizer, a decoder for digitally demodulating an audio file signal from the reception circuit, and a processor for initializing the decoder in response to a loss of a phase lock in the demodulating of the audio file signal and setting the frequency synthesizer at one of a plurality of frequencies to re-establish the phase lock in the demodulating of the audio file signal. The plurality of frequencies are 900MHz range channel frequencies. Preferably, the plurality of frequencies are 905 MHz, 911 MHz, 917 MHz and 923 MHz. The decoder comprises an ~~eight-to-four~~ eight-to-fourteen modulation EFM digital decoder. Demodulating the audio file signal provides a digital audio stream conforming to an I2S audio format. The processor is preferably a microprocessor.